Speakers:
Robert Adamski and Georgios Kardaras, Intel Corporation

Title:
Workload Acceleration with Intel Xeon Scalable Processors and FPGAs

Abstract:
To address the compute and labor-intensive challenges of the innovation cycle, Intel accelerators can be chosen to match the target workload. Intel FPGAs, in particular, can be dynamically tailored specifically for a specific workload, ensuring optimized performance for the workload at hand. This results in efficient performance and improved performance per watt, low-latency and predictable performance. The Acceleration Stack for Intel Xeon CPU with FPGAs is a robust collection of software, firmware and tools, designed and distributed by Intel, to make it easier to develop and deploy Intel FPGAs for workload optimization in the data center. The Acceleration Stack provides optimized and simplified hardware interfaces and software APIs, saving the developers time so they can focus on the unique value-add of their solution.