

AKADEMIA GÓRNICZO-HUTNICZA IM. STANISŁAWA STASZICA W KRAKOWIE

## Comparison of hybrid sorting algorithms implemented on different parallel hardware platforms

Marcin Pietroń Dominik Żurek Maciej Wielgosz Kazimierz Wiatr



## **Motivation**

- Sorting problem is very common in a lot of computer science domains
- Some of sorting algorithms can be parallelized
- Hardware accelerators enable speeding up some stages of sorting algorithms
- Heteregoneus sorting using multicore systems and hardware accelelerators is challenging and should give the best effectiveness



## **Architecture of hybrid sorting**

